

FIG. 2

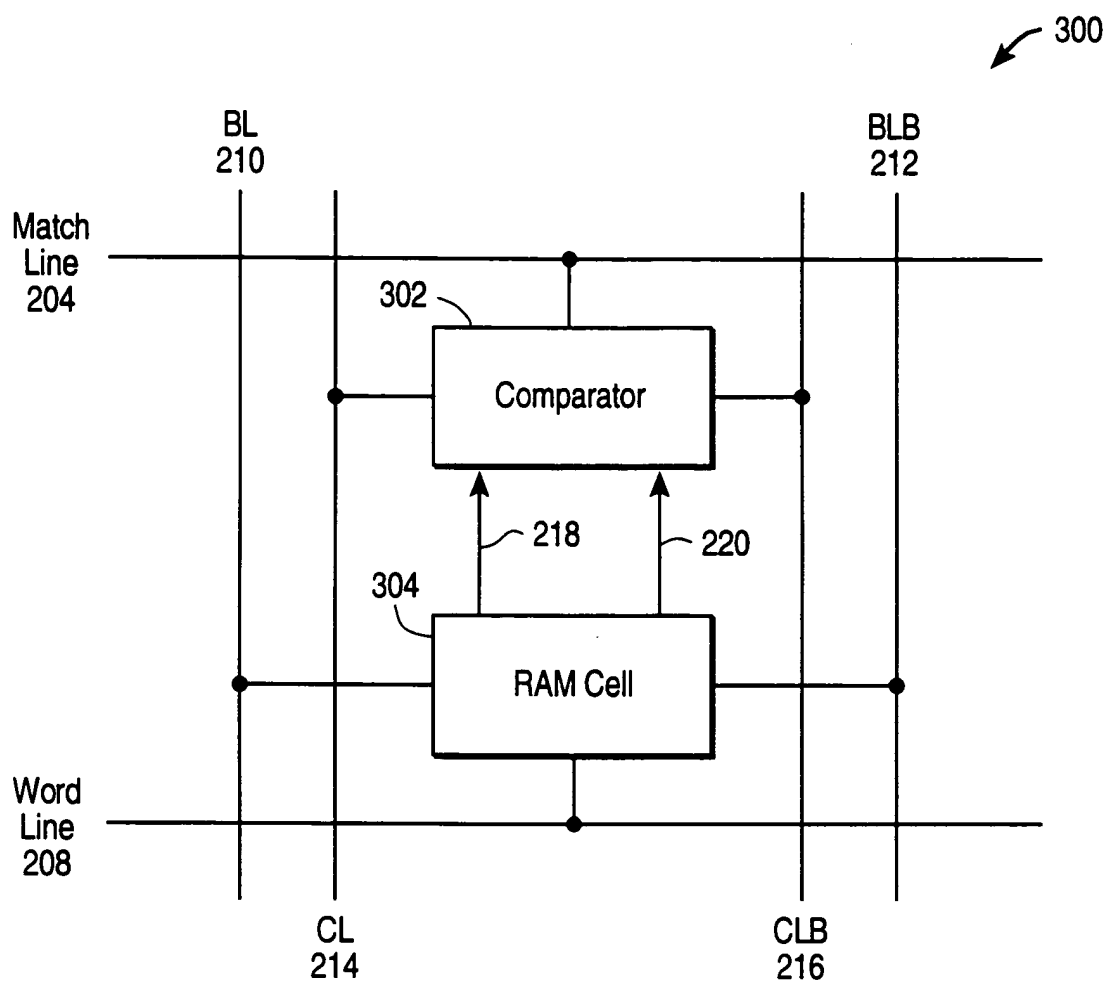


FIG. 3



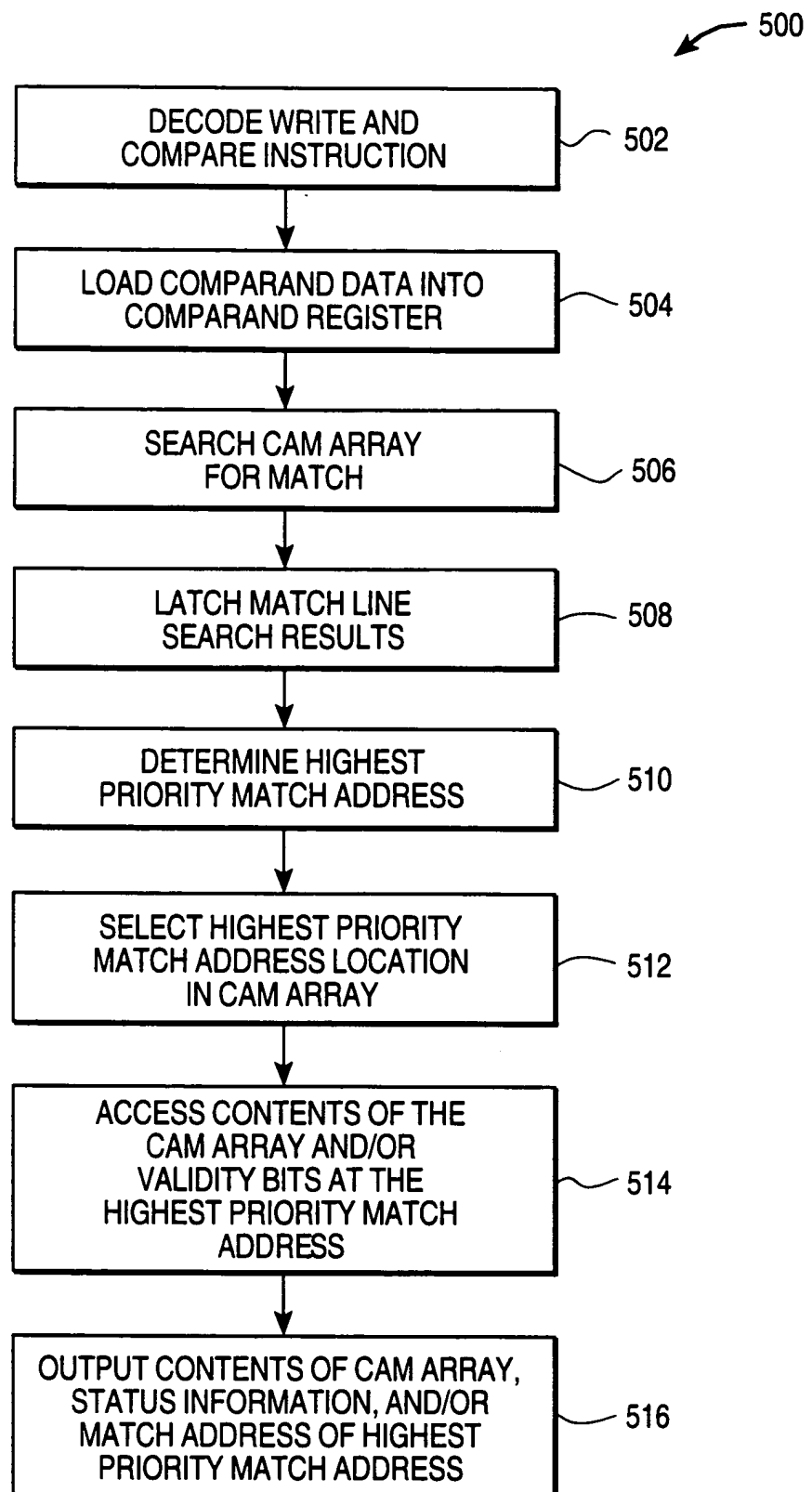


FIG. 5

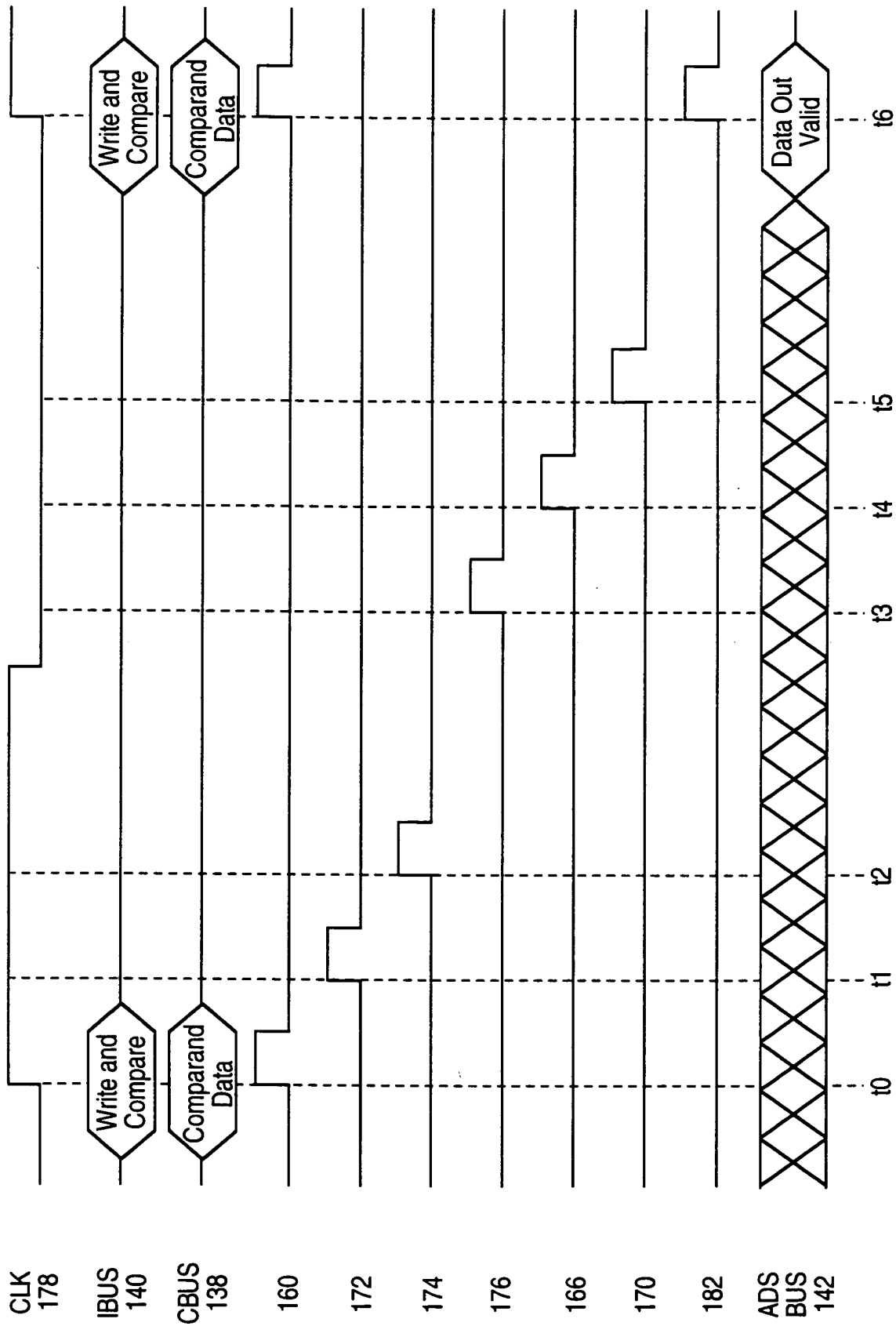


FIG. 6

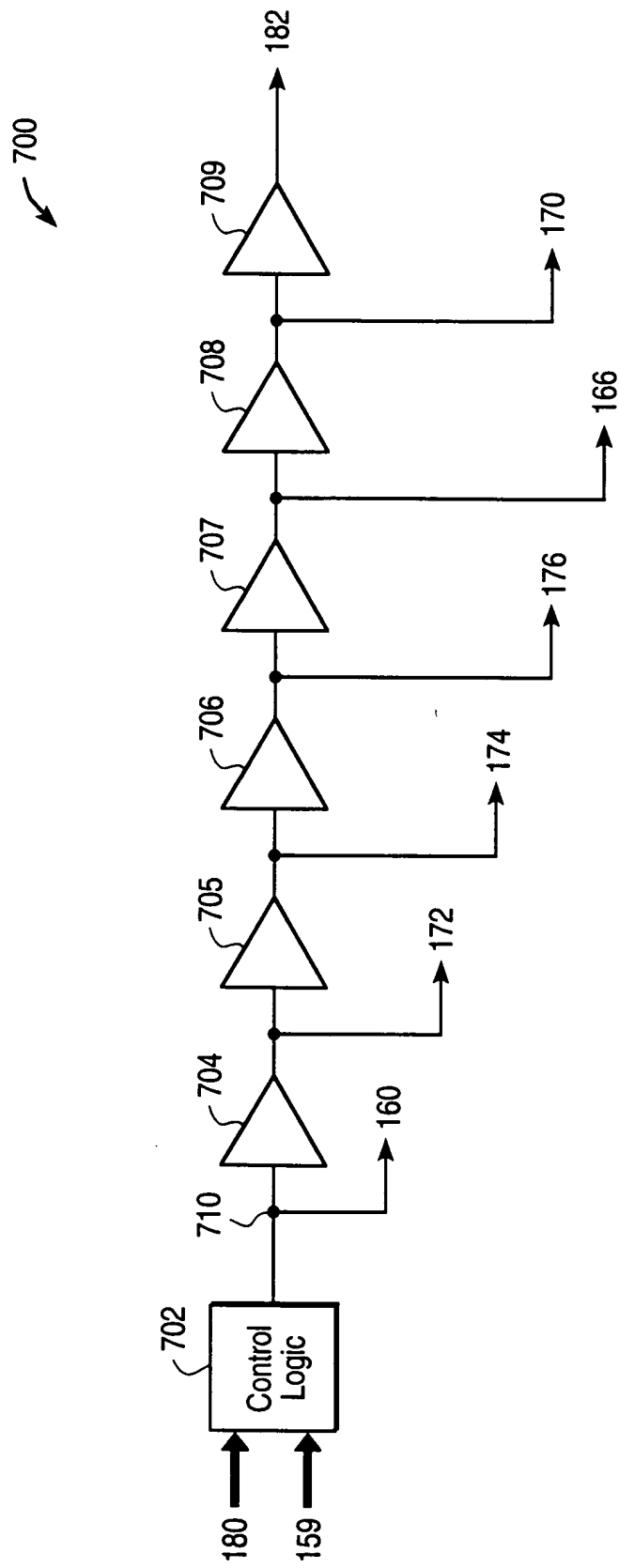


FIG. 7

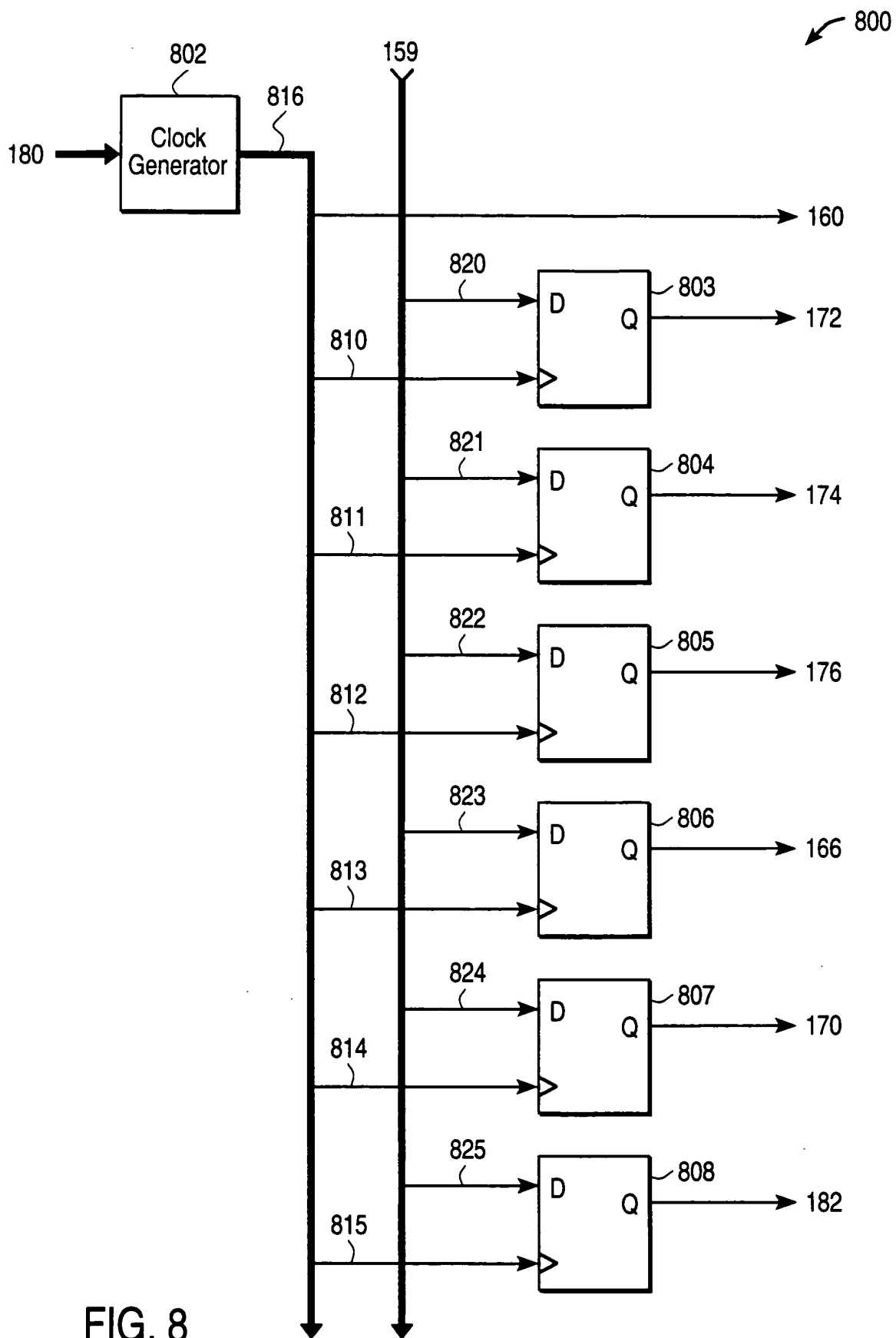


FIG. 8



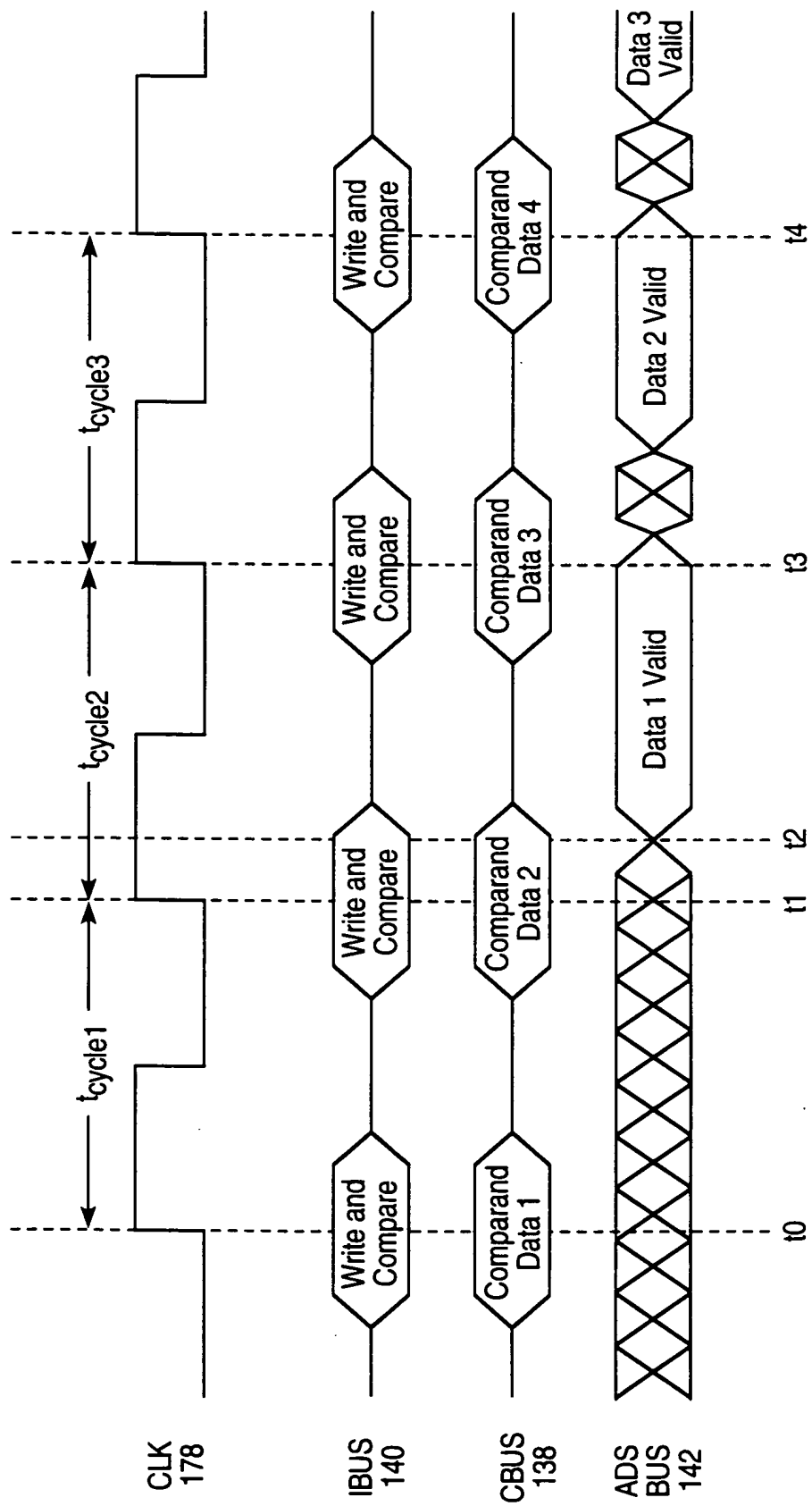


FIG. 9

Bit 63	...	Bit 52	Bit 51	...	Bit 40	Bit 39	...	Bit 37	Bit 36	Bit 35	Bit 34	Bit 33	Bit 32	Bit 31	...	Bit 0
Match Address			Device ID			Reserved			MF	MMF	FF	Skip	Empty	Data from Cam Array 104		

FIG. 10

Bit 63	...	Bit 52	Bit 51	...	Bit 48	Bit 47	...	Bit 0
Match Address			Device ID			Data from Cam Array 104		

FIG. 11